

IN THE SPECIFICATION

Please replace the Title of the Invention to read as follows:

-- CURRENT SWITCHING FOR MAINTAINING A CONSTANT INTERNAL VOLTAGE --

Please replace the following paragraph beginning at page 10, line 6 and ending at page 10, line 15 with the following rewritten paragraph:

-- The voltage reduction circuit 11 includes a p-channel output transistor Q_{P1} in which a power supply voltage V_{DD} is applied to a source and an internal voltage V_{INT} is output at a drain, a differential amplifier circuit 21 for outputting an output voltage V_{ADJ} according to a potential difference between two input terminals to the gate of the output transistor Q_{P1} , a reference voltage generation circuit 22 for inputting a reference potential V_{REF} to one input terminal of the differential amplifier circuit 21, and a voltage divider circuit 23 for inputting an intermediate potential V_{MID} to the other input terminal of the differential amplifier circuit 21. The power supply voltage V_{DD} input into the voltage ~~pressure~~ reduction circuit 11 is reduced by a constant level by a source-drain resistance in the output transistor Q_{P1} and then is output as the internal voltage V_{INT} . --